

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Koga, Hiroki	
Serial No.: N/A (Parent Application 09/991,093)	Group Art Unit (of Parent Application): 2814
Filed: Herewith	Examiner: N/A
Title (as Amended): Semiconductor Device Including an Insulated Gate Field Effect Transistor and Method for Manufacturing the Same	
Attorney Docket No.: N34771600WD1	

PRELIMINARY AMENDMENT

5 Mail Stop Patent Applications  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

10 A. Introductory Comments

The following is a Preliminary Amendment for the attached patent application.

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25 37 C.F.R. §1.10  
I hereby certify that this is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under  
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Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

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